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Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0466

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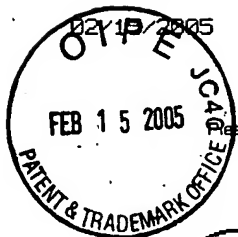
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Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
Date	2/14/05



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NO.394 0007

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Application Number	10/626,366
Filing Date	July-23, 2003
First Named Inventor	Hong Lim
Group Art Unit	2811
Examiner Name	Nitin Parekh
Attorney Docket Number	LSI1P225 / 03-0488

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Typed or Printed Name	SANDEEP JAGGI, Chief IP Counsel, Assistant Corporate Secretary, LSI Logic
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Date	2-14-05

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NO.394 0008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re Application of :**

Hong Lim et. al.

Serial No. :

10/626,366

Group Art Un 2811**Filed :**

July 23, 2003

Examiner: Parekh, Nitin**For :**

Integrated Circuit Package and
Method for a PBGA Package
Having a Multiplicity of Staggered
Power Ring Segments For Power
Connection to Integrated Circuit Die

Attv Docket : LSI1P225 / 03-0466

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CERTIFICATION UNDER 37 C.F.R. 3.73(b)

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Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
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Date: 2-14-05

Respectfully submitted,

Sandeep Jaggi
Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION